



Anekant Education Society's

Tuljaram Chaturchand College
of Arts, Science, Commerce, Baramati
(Autonomous)

DEPARTMENT OF ELECTRONICS

(Faculty of Science and Technology)

Minutes of Board of Studies Meeting No. 2

Date of Meeting: 29/05/2020

Venue: Department of Electronics

May, 2020

Anekant Education Society's

**Tuljaram Chaturchand College of Arts, Science,
Commerce, Baramati
(Autonomous)**

Department of Electronics

NOTICE

Date: 25/05/2020

Board of Studies Meeting

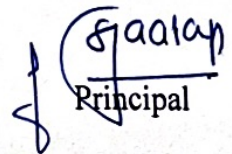
All members of the Board of Studies in Electronics are hereby informed that, as per Notice issued by the college, our BOS meeting is organised on 29th May 2020 in online mode at 12.00 pm. As esteemed members of the board, your presence and input during the meeting would be greatly appreciated.

The agenda of the meeting included the following subjects:

1. To confirm the minutes of the previous meeting held on 08/04/2019.
2. To prepare and approve curriculum of S.Y.B.Sc. and S.Y.B.Sc. (Comp.Sci.) Semester-III and IV (2019 pattern) to be implemented from the academic year 2020-2021.
3. To prepare and approve curriculum of M.Sc.-II Semester-III and IV (2019 pattern) to be implemented from the academic year 2020-2021.
4. To discuss and incorporate the relevant feedbacks of the stakeholders (students, teachers, parents, alumni and employers) in the curriculum.
5. Any other issue with the permission of the chair.



Chairman BOS in Electronics


Principal

Anekant Education Society's
Tuljaram Chaturchand College, Baramati
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Department of Electronics

AGENDA OF THE MEETING

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5. Any other issue with the permission of the chair.

List of Members Presented for the BOS Meeting

The following internal and external BOS members were attended the Board of Studies (Electronics) meeting through online mode held on 29th May, 2020.

Sr. No.	Name of Member	Designation
1.	Dr. Deshpande J. D. Head & Professor, Department of Electronics, T. C. College, Baramati.	Chairman
2.	Mr. V.T. Yalpale Assistant Professor, Department of Electronics, T. C. College, Baramati	Internal Member
3.	Mr. S.H. Gawade Assistant Professor, Department of Electronics, T. C. College, Baramati	Internal Member
4.	Dr. Mrs. Pawar A. M. Assistant Professor, Department of Electronics, T. C. College, Baramati	Internal Member
5.	Mr. Kothawale A. S. Assistant Professor, Department of Electronics, T. C. College, Baramati	Internal Member
6.	Mrs. Rupnawar P. D. Assistant Professor, Department of Electronics, T. C. College, Baramati	Internal Member
7.	Mrs. Gawade S. A. Assistant Professor, Department of Electronics, T. C. College, Baramati	Internal Member
8.	Dr. Sushma Kulkarni Assistant Professor, MSG College, Malegaon,	Expert from other University
9.	Dr. S. N. Helambe Associate Professor, Department of Electronics Devgori College, Aurangabad	Expert from other University
10.	Dr. Hivarkar Capegemini ITC, Pune	Industry Expert
11.	Dr. Vasmbekar Shivaji University, Kolhapur	Expert from other University
12.	Akash Shinde	Meritorious Alumni
13.	Munde Shubham	PG Student
14.	Kajal Adsul	PG Student
15.	Khaire Kiran	UG student

MINUTES OF THE MEETING

As per the Notice issued by the college, the meeting of Board of Studies in Electronics was successfully held on 29th May, 2023 at 12:00 pm in online mode. The meeting took place both online and offline, adhering to the guidelines and protocols set by the college under the guidance of Dr. J. D. Deshpande, Chairman of the Board of Studies in Electronics, the meeting commenced with a warm welcome to all the esteemed members, followed by a brief introduction of the meeting's objectives.

During the meeting, fruitful discussions were held on the items mentioned in the circulated agenda. We are pleased to inform you that the following resolutions were made during the BOS meeting.

1. To confirm the minutes of the BOS meeting held on 8th April 2019.

Dr. J. D. Deshpande read the minutes of the BOS meeting held on 8th April 2019 and put forward to the BOS members for the approval.

Resolution No. 1: The minutes of the previous Board of Studies meeting were approved and confirmed.

2. To prepare and approve curriculum of S. Y. B.Sc. and S. Y. B.Sc. (Computer Science), Semester III and IV (2019 pattern) to be implemented from the academic year 2020-2021.

The Board of Studies (BOS) members meticulously designed and crafted the curriculum for Semester III and IV of the S. Y. B.Sc. and S. Y. B.Sc. (Computer Science). This preliminary draft was then shared with all BOS members for their input and suggestions to enhance its quality. During the meeting, Dr. Pawar A. M. presented the curriculum on a course-by-course basis, 30:20 pattern of evaluation for all classes. The recommendations and valuable insights provided by the BOS members were thoughtfully incorporated into the curriculum. In light of the constructive suggestions offered by the BOS members, the curriculum structure underwent necessary revisions. After thorough deliberation and careful consideration, the curriculum of following courses was presented for approval during the BOS meeting.

Class	Pattern	Semester	Course Code	Course Title	Course Type	No. of Credits
S.Y.B.Sc. (Comp.Sci.) (Electronics)	2019	III	CSEL2101	Paper-I: Fundamentals of Memory organization and Embedded System	Theory	03
			CSEL2102	Paper-II: Analog Electronics	Theory	03
			CSEL2103	Paper- III: Practical Course	Practical	03
		IV	CSEL2201	The 8051 Architecture, Interfacing & Programming	Theory	03
			CSEL2202	Advanced Communication Techniques	Theory	03
			CSEL2203	Paper- III: Practical Course	Practical	03
S.Y.B.Sc. (Electronics)	2019	III	ELE2301	Paper-I: Analog circuit design	Theory	03
			ELE2302	Paper-II: Digital circuit design	Theory	03
			ELE2303	Paper-III: PRactical course	Practical	03
		IV	ELE2401	Paper-I: Instrumentation	Theory	03
			ELE2402	Paper-II: Communication Electronics	Theory	03
			ELE2403	Paper- III: Practical Course	Practical	03

Resolution No. 2: The curriculum for S. Y. B.Sc. and S. Y. B.Sc. (Computer Science), Semester III and IV (2019 pattern) has been unanimously approved by all members of the BOS.

3. To prepare and approve curriculum of M.Sc.-II Semester III and IV (2019 pattern) to be implemented from the academic year 2020-2021.

The Board of Studies (BOS) members meticulously designed and crafted the curriculum for M.Sc.-II Semester III and IV (2019 pattern) program well in advance of the BOS meeting. This preliminary draft was then shared with all BOS members for their input and suggestions to enhance its quality. During the meeting Dr. S. N. Patil and Dr. A. S. Kothawale presented the curriculum on a course-by-course basis, and the recommendations and valuable insights provided by the BOS members were thoughtfully incorporated into the curriculum. During the discussion, some minor changes were suggested by the board members. The board thoroughly discussed and finalized the syllabus for the courses of M.Sc. Part-II, Semester-III and IV. In light of the constructive suggestions offered by the BOS members, the curriculum structure underwent necessary revisions. After thorough deliberation and careful consideration, the curriculum of following courses was presented for approval during the BOS meeting.

Class	Pattern	Semester	Course Code	Course Title	Course Type	No. of Credits
M.Sc.-II	2019	III	ELE4301	Advanced communication electronics	Theory	4
			ELE4302	Advanced Embedded system	Theory	4
			ELE4303	Digital signal processing	Theory	4
			ELE4304	Programmable logic controllers and applications	Theory	4
			ELE4305	Practical course - V	Practical	4
			ELE4306	Practical Course -IV	Practical	4
		IV	ELE4401	Control System	Theory	4
			ELE4402	Advanced Power Electronics	Theory	4
			ELE4403	Mechatronics and Robotics	Theory	4
			ELE4404	Wireless sensor and Network	Theory	4
			ELE4405	Project	Project	8


Resolution No. 3: The curriculum for M.Sc.-II Semester III and IV (2019 pattern) has been unanimously approved by all members of the BOS.

4. To discuss and incorporate the relevant feedbacks of the stakeholders (students, teachers, parents, alumni and employers) in the curriculum.

The Chairman initiated the discussion by highlighting the importance of incorporating feedback from both Alumni and current students in the syllabus design process. The department created a curriculum feedback form and distributed it to students, teachers, parents, alumni, and employers. Subsequently, the stakeholders filled out the feedback forms. It was noted that their insights and suggestions would greatly contribute to creating a curriculum that aligns with the needs and expectations of the students. Therefore, the BOS members reviewed the feedback and suggestions given by the alumni and students and incorporated the relevant suggestions into the curriculum of S.Y.B.Sc., S.Y.B.Sc.(Computer Science) and M.Sc. II Electronics.

Resolution No. 4: Considered and Approved

The meeting of BOS concluded with the vote of thanks by Prof. S. H. Gawade


Chairman
Board of Studies


IOAC
Coordinator
Internal Quality Assurance Cell
Tuljaram Chaturchand College of
Arts, Science and Commerce,
Baramati (Pune)-413102


Principal

